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APPLICANTS

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** CONTINUING DATA ***** VFI LV
 This application is a DIV of 09/959,576 10/30/2001 ABN
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** FOREIGN APPLICATIONS ***** VCFE LV

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <i>[Signature]</i> Initials <i>LV</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 35	TOTAL CLAIMS 37	INDEPENDENT CLAIMS 6
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TITLE
 Method of manufacturing a semiconductor integrated circuit device having a plurality of wiring layers and mask-pattern generation method

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